

DATA DECODING APPARATUS AND METHOD OF SAME

ABSTRACT OF THE DISCLOSURE

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A data decoding apparatus having a simple circuit configuration and processing data at a high speed by detecting and deleting a marker code by a simple circuit at a high speed. A marker remover deletes the marker from 10 a data stream containing the input marker and generates a data stream having 32 bits of fixed length comprised by only data. Also, 4 bits of a marker flag indicating a type and a location of a marker are generated and added to each data. The 32 bits of data are input via a buffer 15 RAM to a Huffman decoder, the restart marker is detected from the marker flag, and the decoding is carried out while appropriately resetting the DC component value based on this. At the decoding, processing for deleting the restart marker in the data string becomes 20 unnecessary, thus the decoded data can be successively output. Also, the circuit configuration becomes simple too.

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